

REMARKS

Claims 1-3, 5-8, 15-19, and 22-23 are pending in this application. By this Amendment, new claim 23 is added. No new matter is added. Reconsideration in view of the above amendments and the following remarks is respectfully requested.

The Advisory Action maintains the March 9 Office Action's 35 U.S.C. §102(b) rejection of claims 1-8, 15-19, and 22 over Iwafuchi (U.S. Patent Application Publication No. 2002/0096994). Applicant respectfully traverses this rejection.

Specifically, Applicant asserts that Iwafuchi fails to disclose a method of manufacturing an electro-optical device, the method including at least the steps of forming a plurality of chips each of which includes a drive circuit on a *first* substrate, forming wires for connecting one of the plurality of chips with a plurality of pixel electrodes, wherein the plurality of pixel electrodes are formed on a *second* substrate, and transferring at least one of the plurality of chips from the first substrate onto the second substrate, as recited in independent claim 1.

The Advisory Action asserts that Iwafuchi discloses forming a plurality of chips with a plurality of pixel electrodes (247, 248) on a second substrate, and transferring at least one of the plurality of chips from the first substrate onto the second substrate (paragraphs [0235] and [0253] with reference to items 247 and 248, Figs. 35-36 of Iwafuchi).

In stark contrast, Iwafuchi, in paragraph [0235], discloses that wiring layers 247 and 248 form *wiring* for supplying signals to respective n-electrodes and p-electrodes of each light emitting diode. The p-electrodes and n-electrodes are items 244 and 249 (see Fig. 36) of a light emitting diode to be mounted on an image display unit (see paragraph [0243]). Moreover, in paragraph [0044], Iwafuchi discloses that elements of the light emitting diode are formed on a substrate *different from the wiring board*. Thus, Iwafuchi fails to disclose forming a light emitting diode (pixel electrodes) on a wiring board (second substrate).

Furthermore, Iwafuchi, in paragraphs [0176]-[0184] with reference to Figs. 5-12, discloses forming electrodes 55 and 56 on a light emitting device which is located on a sapphire substrate 51, then transferring the light emitting device including the electrodes to a temporary holding board 60, and then with an attracting jig 70, transferring the light emitting device including the electrodes to a wiring board 80.

Accordingly, Iwafuchi fails to disclose forming a plurality of chips on a first substrate, forming a plurality of pixel electrodes on a second substrate, and transferring at least one of the plurality of chips from the first substrate onto the second substrate, as recited in claim 1.

In accordance with the above remarks, Applicant submits that independent claim 1 defines patentable subject matter. Claims 2-3, 5-8, 15-19, and 22-23 depend from claim 1, and therefore, also define patentable subject matter, as well as for the additional features they recite. Thus, Applicant respectfully requests that the Examiner withdraw the §102(b) rejection.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-3, 5-8, 15-19, and 22-23 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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Attachments:

Petition for Extension of Time
Request for Continued Examination

Date: July 7, 2006

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